Fault Tolerant Cost-effective Carrierless Stochastic Synthesis of Voltages and Currents in Multi-Cell Multilevel Converters via the Central Limit Theorem

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Abstract—The paper presents - with experimental evidence - a highly fault tolerant, cost-effective and carrierless solution to synthesize voltages (or currents) across the series (or parallel) connection of several voltage-source (or current-source) cells. Hundreds of voltage-source cells already characterize modern multilevel HVDC converters and structures based on current-source cells could become industrially meaningful too, if sufficient advancements in semiconductor and magnetic materials will occur. Exactly by relying on a large number of cells, the key approach is characterized by an unconventional absence of any determinism. The voltages (or currents) of all cells are generated as discrete independent random variables whose distribution is parameterized by the desired reference waveform to be synthesized. This is a radical difference from the already existing random PWM modulations, where only the switching instants are aleatory. The essence of the proposed method lies in observing that multilevel converters, which synthesize the desired waveforms through additive linear combinations (e.g., the simplest sum) of several elementary contributions, become naturally ruled by the Central Limit Theorem of the theory of probability. Such a fundamental law of nature assures also that the greater the number of cells, the more the waveform synthesis becomes inherently robust, fault-tolerant and accurate. This property strikingly differs from those of deterministic centrally ruled modulations and it is also achieved with reduced hardware complexity and cost. The proposed method benefits from emerging technologies employing smaller mass-produced cells based on devices capable of improved switching characteristics, like SiC or GaN, thereby suitin g design philosophies characterized by a high number of cost-optimized standard elementary units. Equally important, the proposed synthesis eliminates most physical communication channels (e.g., optical fibers) among the controller and the switches, thereby greatly improving the system reliability. The paper introduces also a hybrid variant of the method conceived for fewer cells.

I. INTRODUCTION

The presently most common approach for synthesizing voltage (or current) waveforms in high-power voltage-source (or current-source) multilevel converters, based on several elementary cells connected in series (or parallel), is still deterministic and it relies on a multiplicity of synchronized carriers, which can be easily by the hundreds nowadays. A well known example of such class of converters is the M2LC structure by Prof. R. Marquardt [1]–[4]. Such a methodology poses significant reliability requirements on the main controller, which must assure a significant level of availability in the difficult scenario characterized by a large number of signal lines to and from the controllable switches. These physical communication channels are easily by the hundreds as well nowadays, even when multiplexing techniques together with addressing schemes of the cells are used [5]. Still today, several optical fibers and their associated optoelectronic transmitters/receivers are the most common physical media for the switching signals, as well as for the feedback/acknowledge signals, and to and from the controllable switches. Some realizations generate the switching signals inside the cells upon reception of the voltage command. Other realizations let the central controller to command directly each single switch. Whichever the realization, the present concepts for multilevel synthesis are intrinsically deterministic and the higher the number of cells the greater the complexity of the command chains - then their costs - become in order to assure the needed high system availability, which is so crucial for power system applications [6]. The envisaged future ratings of HVDC and DC-GRID converters can easily demand a so high number of cells, for which it will become increasingly expensive to guarantee a reliable "deterministic" control hardware capable of maintaining an acceptably low probability of critical malfunctions in the command chains. By eliminating such needs [4], [6] - some of which are consequences of approaching the waveform syntheses in a deterministic way - one can greatly improve the system availability and reduce the system life cycle costs. The paper describes a different and non-deterministic approach to the synthesis of multilevel waveforms in the aforementioned class of converters. Its principles were already disclosed in the publicly available document [7] by Research Disclosure. The approach owes its attractive characteristics to an unconventional absence of determinism. Its conceptual roots lie in the Central Limit Theorem (CLT) of the Theory of Probability [8], a fundamental law of nature ruling many phenomena that manifest additive effects in large multitudes of similar, yet independent, elementary physical entities. When several voltage-source (current-source) cells are in series (parallel), they constitute a physical system composed by many elementary entities indeed. If the interest is on the total voltage (current) at the two extreme terminals of the series (parallel), this becomes exactly a scenario where such a total voltage (current) can be ruled stochastically - by relying on the CLT - simply because the total voltage (current) is the sum of all single cell voltages (currents).

Random PWM methods based on probabilistic distributions of the switching intervals are well known in literature since long time [9]–[11] and were also applied to multilevel converters...
[12] in order to achieve desired performance effects, often related to exigencies of creating spread-spectrum waveforms for mitigation of EMI and/or structure-borne mechanical noise.

The proposed synthesis differs radically from those methods because it does not introduce randomness in the switching instants, but truly in the voltage (current) values generated by each cell. Each single cell indeed has the role of a discrete random variable - independent from all others - whose possible values are the voltage (current) levels that the cell can synthesize. Owing to the CLT, the large number of cells assures the accuracy of the total voltage (current) across their series (parallel) connection. The paper will also describe a hybrid approach derived to achieve similar performances with reduced number of cells that would not otherwise allow the manifestation of the effects of the CLT.

All figures possess a resolution of 400 DPI. Their details are easily readable by magnifying the electronic version of the paper.

II. THE MAIN PRINCIPLE

The approach is inspired by a corollary of the CLT [8], the fundamental law of nature ruling stochastic phenomena whose effects manifest themselves as sum of independent random contributions. The more numerous such random contributions are - modelled as random variables - the more the distribution of their sum approximates a Gaussian (“Normal”) law centered around an expectation value equal to the sum of the expectation values of the single independent variables. The pivotal aspect stated by the CLT concerns the variance of such a sum, which becomes smaller and smaller as the number of added variables becomes greater and greater, even if their probability distributions are different [8]. This concept has been simply observed, recalled and applied by the authors to the sum of the voltages of several voltage-source cells, when they are connected in series to form an “arm” of a multilevel voltage-source converter. The observation can be equivalently applied to the sum of the currents of several paralleled current-source cells composing an “arm” of a multilevel current-source converter. These two dual networks are illustrated in Fig. 1 and from now onwards only the case of voltage-source multilevel converters will be discussed, being the case of current-source ones derivable by straightforward physical and conceptual duality.

The principle is elucidated here for the simplest case constituted by the series of voltage-source two level cells, each one capable of synthesizing a voltage of both polarities, $+V_{dc}$ or $-V_{dc}$, with $V_{dc} > 0$. More in general, the concept is applicable to any type of cell capable of generating at least two discrete voltage (or current) levels. The $N_c$ cells are all independent among each other, no predefined commutation pattern and no exchange of information among them exist. This peculiarity leads to less components and to a very robust switching scheme thereby increasing the system tolerance towards cell failures. Each $j^{th}$ cell, with $j \in \{1..N_c\}$, possesses an internal uniform independent (pseudo)random generator $R_j$ in $[0,1]$ and it receives, from an external agent (e.g. the central converter controller), just the “reference probability” $p(t)$ for the value $+V_{dc}$. $p(t)$ is broadcasted identical to all cells. The denomination means that $p(t)$ is the probability of the event: "Output voltage $= +V_{dc}$ at the instant $t^*$", which the external agent desires to be identical for all independent cells. The reader already notices a significant simplification brought by the concept because the external agent needs to broadcast just one analog command signal to all cells belonging to a specific "arm", a feature that, for example, can be easily achieved by a dedicated, encrypted and interference-resistant radio link for each "arm". The synthesis of the cell voltages occur at discrete time instants separated by the interval $T_s > 0$, not necessarily constant. Independently inside each single $j^{th}$ cell, and independently for each specific discrete time instant $t^*$, the value of the random generator $R_j(t^*)$ at $t^*$ is observed and compared with the received probability $p(t^*)$ at $t^*$. Finally, the voltage generated by the cell at its output terminals - and kept constant during the whole time interval $[t^*, t^* + T_s]$ - is chosen according to Eq.1a

$$\begin{align}
E[v_j(t^*)] &= (2 \cdot p(t^*) - 1) \cdot V_{dc} \quad \text{b) }
\end{align}$$

From this description and the chosen discrete probabilistic scheme is immediate to infer that the $N_c$ voltages generated by all $N_c$ cells are discrete-time independent stochastic processes defined, at each discrete instant $t^*$, by the binary random variables from Eq.1a. The ensemble expectation of each process at the instant $t^*$ is given by Eq.1b. When the value $p(t^*)$ "broadcasted" to all cells by the external agent (e.g. central controller) is 1, then all cells synthesize $+V_{dc}$ with probability 1 and the total voltage $v_{TOT}(t^*)$ is the maximum that the "arm" composed by the $N_c$ cells in series can provide. Conversely, when the value $p(t^*)$ is 0 all cells synthesize $-V_{dc}$ with probability 1 and $v_{TOT}(t^*)$ is the minimum that the "arm" can provide. For any other value $p(t^*)$ strictly inside $[0,1]$ some cells synthesize $+V_{dc}$ and some others $-V_{dc}$ without any possibility to determine a-priori which cell does what. The switching decision inside each cell is purely local and independent from those of the other cells. Nevertheless, when $N_c$ is sufficiently large such a "switching anarchy" at the level of the single cells does not matter for
the ultimate quantity of interest, which is only $v_{\text{TOT}}(t^*)$. The nature itself indeed - acting through the model represented by the CLT - assures that the higher $N_c$, the less probable it is that the random variable $v_{\text{TOT}}(t^*)$ deviates significantly from its expectation $(2p(t^*) - 1) \cdot V_{dc} \cdot N_c$, simply because the variance of $v_{\text{TOT}}(t^*)$ is monotonically and non-linearly decreasing with $N_c$ [8].

It descends, as corollary of greatest importance, that the higher $N_c$, the more probable it becomes that the ensemble average of the stochastic process $v_{\text{TOT}}(t)$ - which represents the voltage synthesized by the "arm" - remain very close to the deterministic desired reference voltage $V_{\text{ref}}(t) = (2p(t) - 1) \cdot V_{dc} \cdot N_c$. Such a voltage indeed is univocally mapped by the broadcasted "reference probability" $p(t)$, which is a deterministic control signal. The $N_c$-dimensional switching configuration composed by all cells in the "arm" certainly evolves fully randomly as time elapses (it is the set of $N_c$ stochastic processes, each one associated to one specific cell) but, despite this randomness, the CLT assures that, when $N_c$ is sufficiently large, the synthesis of the sum $v_{\text{TOT}}(t)$ can be very accurate in probabilistic sense, and the more so the higher the greater $N_c$ is. No centralized and/or deterministic switching commands (e.g. through fiber optics) and no communication among cells are needed. Additionally, the reliable practical realizations of the present day deterministic schemes become more expensive the higher $N_c$ is, whereas the cost grows much less for the proposed stochastic synthesis. This simply because it avoids the whole infrastructure otherwise needed to guarantee reliable centrally-decided switching signals, including the reconfiguration of the carrier schemes when cells fail. Here, under total randomness, no carrier scheme is present, hence no change in deterministic modulation patterns is needed. Furthermore the higher $N_c$, the more robust and more insensitive to $N_c$ itself - as well as to $N_c$ variations caused by cell failures - the synthesis becomes.

The time interval $T_s$ of repetition of the stochastic synthesis may resemble the equivalent period of any Pulse Width Modulation (PWM) scheme. Nevertheless - differently from many deterministic PWM syntheses, including several Space Vector Modulation (SVM) cases - $T_s$ does not need to be constant at all and it can be changed dynamically during the operation of the converter, provided that a maximum value, guaranteeing a sufficient time-discretization of the reference voltage, is not exceeded. The stochastic synthesis performed at $t^*$ is completely independent from the other ones performed in $t^* - T_s$ and in $t^* + T_s$, as well as in any other discrete instant. The reference probability $p(t)$, univocally related with the desired reference voltage, can change abruptly between two consecutive discrete instants. As a consequence, significant variations in the total "arm" voltage can occur as frequently as the chosen $T_s$ period and, in pure principle, $V_{\text{ref}}(t)$ could swing between the extreme voltage values $-V_{dc} \cdot N_c$ and $+V_{dc} \cdot N_c$ within a single $T_s$ interval. In reality this would be rarely allowed because of physical limitations on the maximum allowed $dv/dt$, like it is often the case imposed by the lifetime of the insulating materials. The nature of the proposed method allows to synthesize any voltage waveform sampled at $T_s$ rate, even with possibly variable $T_s$. Clearly when the structure of at least some cells creates non symmetrical voltage values (e.g. half-bridge cells) also the total output voltage will be consequently influenced. Variants of the approach can reduce the commutations by introducing local states at cell level [7].

III. SUITABILITY OF THE APPROACH FOR THE FUTURE SWITCH, PRODUCTION AND SYSTEM TECHNOLOGIES

I) The "reference probability" $p(t)$ is a single scalar signal that just needs an elementary form of unidirectional communication from the centralized external agent (e.g. central controller) to all cells. A single, encrypted and interference-resistant, radio broadcasting channel assigned to each "arm" inside the converter suits the purpose.

II) Since the cells are many and one relies on a sufficient redundant number of them, for the purpose of synthesizing $v_{\text{TOT}}(t)$ it is not even necessary that the external agent is informed about the feedback signals from the switches that acknowledge the commutation. All such signals are always imperative in high power, but now they can remain confined inside each cell and can be dealt with locally according to the protection strategies at single cell level. Certainly, for purposes of diagnostics and protection of the whole converter some information must be eventually sent to the external agent. But such an information is much less, it requires much less channels and can be more delayed, than in the present cases of totally "disciplined" deterministic switching strategies. In such classic designs indeed the controller must be immediately informed about the failed switches and cells, in order to reconfigure the deterministic modulation pattern rapidly. The cost of assuring this performance increases with the number of cells. Conversely, the proposed stochastic synthesis naturally eliminates this need for a sufficiently large number of cells and the more cells exist the more robust against cell failures the scheme naturally becomes.

III) Like other well-known random PWM schemes [9]–[11] the method offers "spread spectrum" possibilities in the synthesized voltages and the consequent currents. This simplifies the ElectroMagnetic Interference (EMI) and filtering aspects already at the single cell level. The possibility to use a variable synthesis interval $T_s$ improves this aspect further.

IV) The method is clearly suitable for "arms" composed by a large number of cells of possibly smaller ratings to realize the converters for power systems applications, like HVDC. This suits well mass production of several smaller standardized cells (cheaper by economy of scale), high design flexibility and it favours the essential fault tolerance in such applications. In turn, cells of reduced ratings suit better emerging faster semiconductor switches with minimal switching losses like SiC or GaN, for which also switching frequencies and losses will no longer be a limitation as today.

V) Extensions of the presented principle can be conceived in which different, or even additional, sources of randomness are introduced in each cell. One can conceive for example variants that use the phases of the local oscillators as additional random variables, or variants that introduce probabilistic correlations among the cells or, again, variants using different distributions for the random generators.
Figure 2: Simulated stochastic synthesis of a 50 Hz phase voltage with 1/6 third harmonic injection and desired transient fast pulse, for an "arm" of a ±320kV HVDC converter composed by 400 cells with 1650 V DC-link voltage, each one based on 3.3 kV switches. Please observe the good synthesis of the transient pulse willingly added to the reference probability in order to show the fast response capability of the method.

IV. OBSERVATIONS AROUND THE HEAT GENERATION

Some readers might object that the method is prone to demand a greater number of switching events than what is already possible with the multilevel deterministic syntheses of today; hence it would lead to excessive conversion of energy in heat form, which is often considered wasted in several applications. This conclusion belongs to narrow visions. The aspect of "losses" indeed must be evaluated over a much broader time and technological horizon in a truly system context, way more than what characterizes today’s common applications. It should be recalled indeed that the undesired, yet unavoidable, conversion of energy into heat is ultimately an aspect related to the second principle of thermodynamics and to material technologies. Since ever the continuous development of such technologies brings to a steady reduction of the "losses" through the years. Conversely, the problem of reaching a satisfactory deterministic control of an increasingly complex modular systems constituted by thousands of similar entities is a much more fundamental aspects of nature. The statistical formulation of thermodynamics, and of its second principle again, tell us since decades how difficult such a pretence will become. It will become increasingly expensive to control reliably via deterministic approaches converters that evolve towards complexities more characteristic of multicellular organisms. The proposed approach has been conceived for future material technologies and highly modular converters. One must not narrow the vision to losses only.

V. THE METHOD APPLIED TO A REALISTIC NUMBER OF CELLS PER ARM IN MODERN HVDC CONVERTERS

The stochastic synthesis has been firstly simulated for a realistic scenario of a HVDC converter based on cascaded-cell structure, rated for ±320kV HVDC link voltage and using single 3.3 kV switches in each cell. This choice can justify a design based on average DC-link voltage for each cell around 1650 V with consequent \( N_c \approx 400 \) cells. The main quantities are shown in Fig. 2 whose lowermost graph represents the "reference probability" \( p(t) \) broadcasted to all cells by the central controller. The uppermost graph compares the desired reference voltage (magenta) - a scaled version of \( p(t) = 1/2 \) - with the stochastic synthesis repeated every \( T_s = 250 \mu s \) (blue). The reader can observe how the willingly requested fast transitory pulse is well synthesized too, as permitted by the chosen interval \( T_s \). The intermediate graph shows, with different colours, six realizations of the stochastic processes representing the individual voltages synthesized by six cells, selected two at both extremes and two in the middle of the arm. It is possible to observe how, as soon as the desired reference voltage becomes close to the maximum or minimum values deliverable by the "arm", the cells synthesize one of their two possible output voltage values with significant more likelihood than the other. This desired behaviour originates from the broadcasted "reference probability" \( p(t) \) of the event "Output voltage = \( +V_{dc} \) at the instant \( t \)" that is computed univocally from the reference voltage as: \( p(t) = \frac{V_{ref}(t)}{2V_{dc}N_c} + \frac{1}{2} \).

VI. APPLICATION TO A MULTI-STAGE M2LC CONVERTER WITH REDUCED STORED ENERGY IN CAPACITORS

The results presented in section V addressed the case of "arms" composed by cells with constant DC-link voltages. Conceptually, this implies cells capable of exchanging a significant amount of energy with the surrounding environment, not only through their output terminals, but also through additional sources connected to their DC-links. In turn, for operation over long time horizons such additional sources would require to exchange the significant amount of energy among themselves and/or with other centralized energy supply subsystems. In order to respect the mandatory graph of the converter, these exchanges could not use any galvanic connection different from the series, which involves the two output terminals of each cells. But the higher the HVDC voltage, the more this topological requirement becomes technologically difficult to satisfy because of the impractical insulation levels and number of barriers that would be needed, with consequent reduction of the system reliability too. The pivotal importance of mastering insulation...
technologies and materials is often insufficiently recalled in HVDC literature, which often praises the obvious modularity and scalability of the converters only. The fact that the M2LC [1]–[3] special sub-family of cascaded-cell converters eases so much the insulation endeavours around HVDC applications is one of the main reasons (not the only one) of its success, even more important than its modularity and scalability. The converters belonging to the M2LC sub-family do not need to exchange significant energy across high insulating barriers, as it would be the case for cells in series, each one having the DC-link fed through a dedicated transformer winding. This latter well known multilevel structure indeed would allow, in principle, much broader operation than the M2LC sub-family, but it is technologically very unsuitable for the voltage levels characterizing HVDC applications. Obviously the necessary "balance" of the state variables in the M2LC converters must be provided by the stochastic synthesis too and, for that purpose, it is necessary to investigate which distributions of the local random generators, as well as which local (i.e. at cell level) corrective actions might be necessary to guarantee it. The present section deals with the first investigations on this research path that has begun by simulating the dynamic behaviour of the more general multi-stage M2LC structure shown in Fig. 3, where each "arm" has been operated under stochastic synthesis.

The circuit is composed by a set of $N$ three-phase M2LC converters connected in parallel at their HVDC terminals and independently fed by $N$ three-phase transformers, each one featuring a proper phase shift. The insulation technology of the transformers is not required to withstand DC components of the Electric Field, thereby reducing their costs and broadening the choice of their suppliers. The structure possesses a total of $6 \cdot N$ "arms" where each one of their total $6 \cdot N \cdot N_c$ cells does not exchange energy with separated external sources. For the preliminary investigations a $1 \text{GW}$, $\pm 320 \text{kV}$, multi-stage M2LC composed by 12 phases in $N = 4$ groups has been chosen. Such a structure implies 24 "arms" in total and one phase every $30^{\circ}$. Each "arm" is conceived around low-current 4.5 kV switches (e.g. at reach of the present day SiC technology) with 2133 V nominal DC-link voltage. This design choice implies $N_c = 300$ cells composing each "arm" and consequently 7200 cells present in the whole converter, which would be very expensive to control deterministically with high reliability. The overall specific energy stored in the capacitors, and conventionally referred to the unit of rated converter power, is chosen to be just 10 kJ/MVA, which is a significantly lower value than the typical 30-40 kJ/MVA for a single M2LC converter [4]. Figures. 4 and 5 show selected results of the stochastic synthesis with $T_s = 250 \mu s$ applied.
to this circuit. One observes that the maximum ripple on the differential (HVDC) voltage remains below 2.5% and the maximum ripple among all capacitor is naturally high by willful choice of reduced stored energy.

The theory of stochastic processes rigorously demands that the frequency decomposition be associated to each single realization only, it is not a property of the process as a whole. Nevertheless, one often observes that in some stochastic phenomena the same frequencies (in continuous spectra, for the realizations are not periodic functions in general) occur in most realizations of the process. This observation often leads to a common - yet not rigorous - empirical definition of frequency components also for the stochastic process as a whole, i.e. the conceptual set of all realizations. While always recalling that the introduction of frequency decomposition for a stochastic process lacks conceptual rigour, one can still observe the well known harmonics in the stochastic DC-link voltages shown by Fig. 4. Those harmonics are mainly caused by a deterministic consequence of the M2LC "arm" structure and they are more prominent in this simulated converter because the nominal energy stored inside it has been greatly reduced to just 10 kJ/MVA by design. Such harmonics introduce great correlation among the stochastic processes represented by the capacitor voltages and they are NOT caused by the proposed stochastic synthesis of the arm voltages. For a chosen DC-link capacitance value (here 0.74 mF), the minimum amplitudes of such harmonics are unavoidably influenced by the variation of the total instantaneous energy present in the "arm", which is simply the integral function of the instantaneous power entering the "arm", as shaped by the desired synthesized waveforms.

Essentially, "above" the CLT - which lies at the basis of the stochastic syntheses of the "arm" voltages - the highly correlated contribution of the instantaneous powers entering each "arm" exists. This effect modifies the discrete set of random voltage values that each cell can generate at any given discrete instant. Besides $p(t^*)$, also the varying DC-link voltages strongly affect the expectations of their cell output voltages now, therefore also the expectation of the total voltage $\mu_{\text{TOT}}(t^*)$ of each "arm" is ultimately affected too. Nevertheless, when one considers the total HVDC link voltage and the total grid currents, such an effect is very mitigated by the proper deterministic harmonic cancellations consequence of the transformer phase shifts.

VII. FIRST EXPERIMENTAL RESULTS FOR ONE ARM

A first experimental verification has been performed by implementing Eq. 1a and a pseudorandom uniform generator from [13] on a Xilinx Virtex-6 FPGA with chosen fundamental timebase of 1µs and synthesis repeated every 250µs. The main and auxiliary boards composing the experiment are visible in Fig. 6. As a first experiment just one cell with constant DC-link voltage was implemented under the rationale that, when a stochastic process can be assumed ergodic, just one realization of it already contains - along the time domain - all stochastic properties of the ensemble of all realizations, i.e. the true process itself. Figure 7 shows some periods of the reference phase voltage (magenta) featuring the typical 1/6 third harmonic injection and 50 Hz fundamental frequency, together with the biased output voltage of just one cell (blue), which manifests the effects of Eq. 1a as the rule for its random synthesis. Such a realization of the stochastic process has been recorded for 600s at 1µs sampling interval and then subdivided in time windows, each one wide a single fundamental period. In order to assure the recognition of the proper beginning of the time windows in post-processing, a synchronization pulse (green) with 1µs duration has been added at each raising zero of the reference voltage. For any specific discrete time instant $t^*$ - univocally identified by a delay $\Delta Z(t^*)$ with respect to the beginning of each window - the different $N_c$ values of the cell output voltage at $t^*$ can be added in post-processing to emulate the voltage $\mu_{\text{TOT}}(t^*)$ synthesized at the terminals of the "arm" composed by $N_c$ cells. This technique - entirely based on the hypothesis that the stochastic process represented by the output of just one cell is ergodic - allows to explore the convergence of $\mu_{\text{TOT}}(t^*)$ to $V_{\text{ref}}(t^*)$ for increasing numbers $N_c$ of series connected cells. This without the need to reprogram the FPGA but, mainly, it allows to explore values of $N_c$ so high that could not be implemented on a single Virtex-6 FPGA board nowadays. The results condensed in Fig. 8 were the first experimental confirmation of the method. There the reader can observe one half period of the normalized reference voltage $V_{\text{ref}}(t)$ together with different normalized sums of different realizations of the stochastic process with increasing $N_c$, i.e. number of
The sums of increasing numbers of cell voltages converge to the reference voltage \( V_{\text{ref}}(t) \) (magenta).

The monotonic convergence of the normalized sums to the normalized reference voltage for increasing \( N_c \) is confirmed, in accordance with the dictates of the CLT. After this first experimental verification, the Virtex-6 FPGA was reprogrammed to implement a complete "arm" composed by \( N_c = 512 \) cells with constant DC-link voltages. Each cell has its independent (pseudo)uniform random generator and is ruled by Eq. 1a. The output of all cells were added directly inside the FPGA, since it really emulated their series connection now. One single, properly scaled, analog output was used to measure this sum. The final result is visible in Fig. 9 and it is the most direct demonstration of the applicability of the CLT to the synthesis of waveforms in highly modular multilevel converters.

Subsequently the Virtex-6 FPGA has been reprogrammed again to implement the dynamic model of one single cell with DC-link capacitor - hence no longer constant DC-link voltage - under the stochastic synthesis still represented by the simple Eq. 1a repeated every 250\( \mu \text{s} \). The parameters of the cell belonging to the multi-stage M2LC structure introduced in section VI were already declared in [7] and the main ones are a sinusoidal "arm" current with 466 A peak, as well as 2133 V of nominal average DC-link voltage. Figure 10 shows the DC-link capacitor voltage emulated by the FPGA over a 30s recording, firstly with a willingly unrealistic capacitance of 74\( mF \). Such a high capacitance has been introduced to explore whether voltage divergences, that might be caused by a fundamental problem of non-zero average power entering the arm, already appear under these idealized operating conditions. Indeed if the voltage already diverged in such conditions, with so much energy stored in the DC-link, it would mean that a fundamental conceptual problem would exist and would prevent the coexistence of the stochastic synthesis with the pivotal necessary condition represented by zero average power entering the cell. The waveform shown in Fig. 10 does not manifest divergence behaviour, which is a comforting result for the prosecution of the research. The DC-link voltage recorded, under the same operating conditions, with a more realistic capacitance of 0.74\( mF \) shows a significant excursion that denounces the need of a more sophisticate local control than just Eq. 1a. Proposals for this control were already mentioned and attempted in [7] and should be subject of further research on the proposed stochastic synthesis approach.

**Figure 8:** From the experiment: The sums of increasing numbers of cell voltages converge to the reference voltage \( V_{\text{ref}}(t) \) (magenta).

**Figure 9:** Stochastic synthesis for an "arm" composed by 512 cells in series. It has been fully implemented on one Xilinx Virtex 6 FPGA.

**VIII. HYBRID SYNTHESIS FOR LIMITED NUMBERS OF CELLS, HENCE EXCESSIVE VARIANCE**

If the number of cells \( N_c \) is in the order of magnitude of few decades, well below 100, the beneficial effects of the CLT are still too limited and the variance of the total synthesized voltage remains too high for a stochastic synthesis of proper accuracy. Figure 8 proves this reality once more. The proposed approach for such cases [7] relies on a combination of the stochastic synthesis and of a local - at "arm" level - deterministic contribution that rapidly corrects, a-posteriori, the excessive error likely brought by the stochastic synthesis. The essence of the hybrid approach can be summarized as it follows by referring to Fig. 11. Additional \( K < N_c \) cells are added in series to the existing \( N_c \) fully independent ones, which remain always ruled by stochastic synthesis, as described by Eq. 1a or more elaborated variants of it. Proper system design should assure that \( K < N_c \) is chosen according to a function of the final desired variance of \( v_{\text{TOT}} \). As previously described for sufficiently large \( N_c \) values, at each discrete instant \( t^* \) the stochastic synthesis is firstly applied to \( N_c \) cells only. After the random switching events that define the voltages generated by the \( N_c \) cells have terminated, at the instant \( t^* + t_1\text{set} \) with \( 0 < t_1\text{set} \ll T_s \), the total voltage \( v_{\text{TOT}}(t + t_1\text{set}) \) synthesized...
between the two extreme "arm" terminals is measured and compared with the desired reference voltage \( V_{ref}(t^*) \), which is always associated to the reference probability \( p(t^*) \) broadcasted to all \( N_c \) independent cells, as described in the previous sections. The difference \( V_{ref}(t^*) - V_{TOT}(t^*+t_1) \) is then compensated incrementally by the \( K \) additional cells, starting to employ the cell \( N_c+1 \) for this and proceeding further with the other \( K-1 \) ones, until the variance of \( V_{TOT} \) is reduced to an acceptable value for the most of the interval \([t^*, t^*+T_s]\). More in detail, after the instant \( t^*+t_1 \), the controller ruling the K-cell set - at local "arm" level only - commands the \( (N_c+1)^{th} \) cell to synthesize the proper voltage within its capabilities to reduce the difference \( V_{ref}(t^*) - V_{TOT}(t^*+t_1) \). After the \( (N_c+1)^{th} \) cell has defined its output a new measurement at \( t^*+t_1 + t_2 \) with \( 0 < t_2 \leq T_s \) is determined again whether the new voltage error \( V_{ref}(t^*) - V_{TOT}(t^*+t_1 + t_2) \) is now acceptable in probabilistic sense or additional correction is further needed. If so, then the cell \( N_c+2 \) is properly commanded too and the resulting error measured again at the subsequent instant \( t^*+t_1 + t_2 + t_3 \) with \( 0 < t_3 \leq T_s \). The process is repeated - for a maximum of \( K \) iterations - until the error voltage becomes acceptable in probabilistic sense for the major portion of the duration \( T_s \) of the synthesis interval. Proper design should assure that \( K \) is conveniently smaller than \( N_c \) and that all elements in the set \( \{t_1, t_2, t_3\} \) add to a value acceptably smaller than \( T_s \). This algorithm is still a carrierless, very simple iterative scheme, based on sequential approximations and can implemented on a "local arm controller" which commands the \( K \) additional "compensating cells" only.

IX. CONCLUSIONS

The paper has presented, and given first experimental verification of, an unconventional stochastic approach to synthesize voltage (or currents) in "arms" of highly modular voltage-source (or current-source) multi-cell multilevel converters for high-power HVDC and FACTS applications. The approach is carrierless, it eliminates the critical structuralism in the switching patterns as well as several centralized command lines to the switches, thereby leading to greater system fault tolerance and cost reduction. The higher the number of cells the more the synthesis becomes robust and accurate as natural consequence of the CLT. This is a key difference when compared to the typical deterministic-carrier-based synthesizes whose implementation complexity will unavoidably increase with greater system modularity. Such is the simple natural consequence of pretending to rule deterministically any large complex system, like the multilevel converters are envisaged to become, even more than today. Nature teaches us - e.g. via the statistical thermodynamics model - that ultimately such systems must be ruled stochastically, not deterministically.

The paper has presented the first results of applying the method to multi-stage converters of M2LC structure with cells deprived of separated energy supplies. The first experimental results are encouraging also in this case; they show that the capacitor voltages do not diverge under random operation of the cells. Yet much more research is certainly needed. The paper has also introduced a hybrid variant of the method, which has been specifically conceived for the cases when a too limited number of cells cannot guarantee sufficiently reduced variance in the syntheses via the CLT only.

REFERENCES


